

# POWER COOLING DEVELOPMENT

# Development phases

- R&D phase
- Tests on module 0
- Tests on single VFE modules
- Development and test of module 0 prime system
- Final design

# R&D Phase

- Modification of connector cooling: better conductance between water loop and connector
- Modification of power cooling: copper braids abandoned and cooling pipes directly brazed on the housing
- Phase completed: results presented at last ECAL week

# Test on module 0 and on single blocks

- Presented by J. Cogan

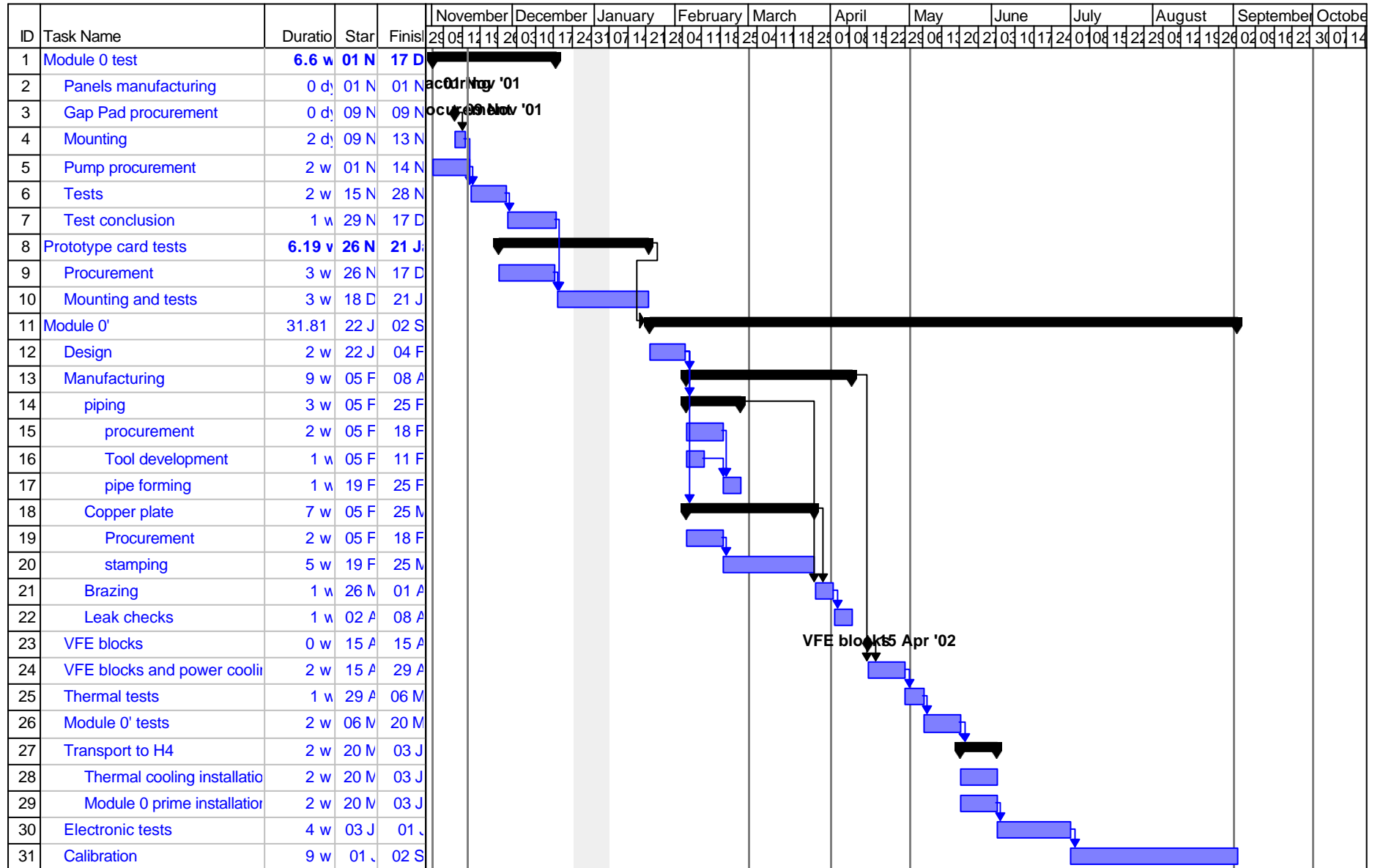
# Module 0' system development

- The design depends on the tests results.
- Probably stamped copper plates
- Must fulfil the thermal specification.
- Different from the final one because the electronic cards will be different
- Foreseen nominal power capacity: 2.5 Watt/channel

# Final Design

- Depends on
  - Module 0 prime test results
  - Final Electronic cards design

# Cooling Schedule



# Water cooling system needs

- M0 and M0' tests:
  - 400 channels (2.5Watt/channel)
  - Regulation and Cooling loop
  - Bat 27 and H4
  - Available
- SM1 functional test
  - 1700 channels (2.5 Watt/channel)
  - Cooling loop only ( $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ , 0.28 l/s)
  - Bat 27 only
  - To be developed



# Water cooling system needs

- SM1 calibration
  - 1700 channels (2.5 Watt/channel)
  - Cooling loop ( $18^{\circ}\text{C} \pm 0.5^{\circ}\text{C}$ , 0.28 l/s)
  - Regulation loop ( $18^{\circ}\text{C} \pm 0.05^{\circ}\text{C}$ , 1.4 l/s)
  - H4 only
  - To be developed

# Water cooling systems milestones

- M0 and M0'
  - 27 building: until 15 May 2002
  - H4: 1 June to 1 September 2002
- SM1 functional tests (build 27): from 1 February 2003
- SM1 calibration (H4): from 1 March 2003